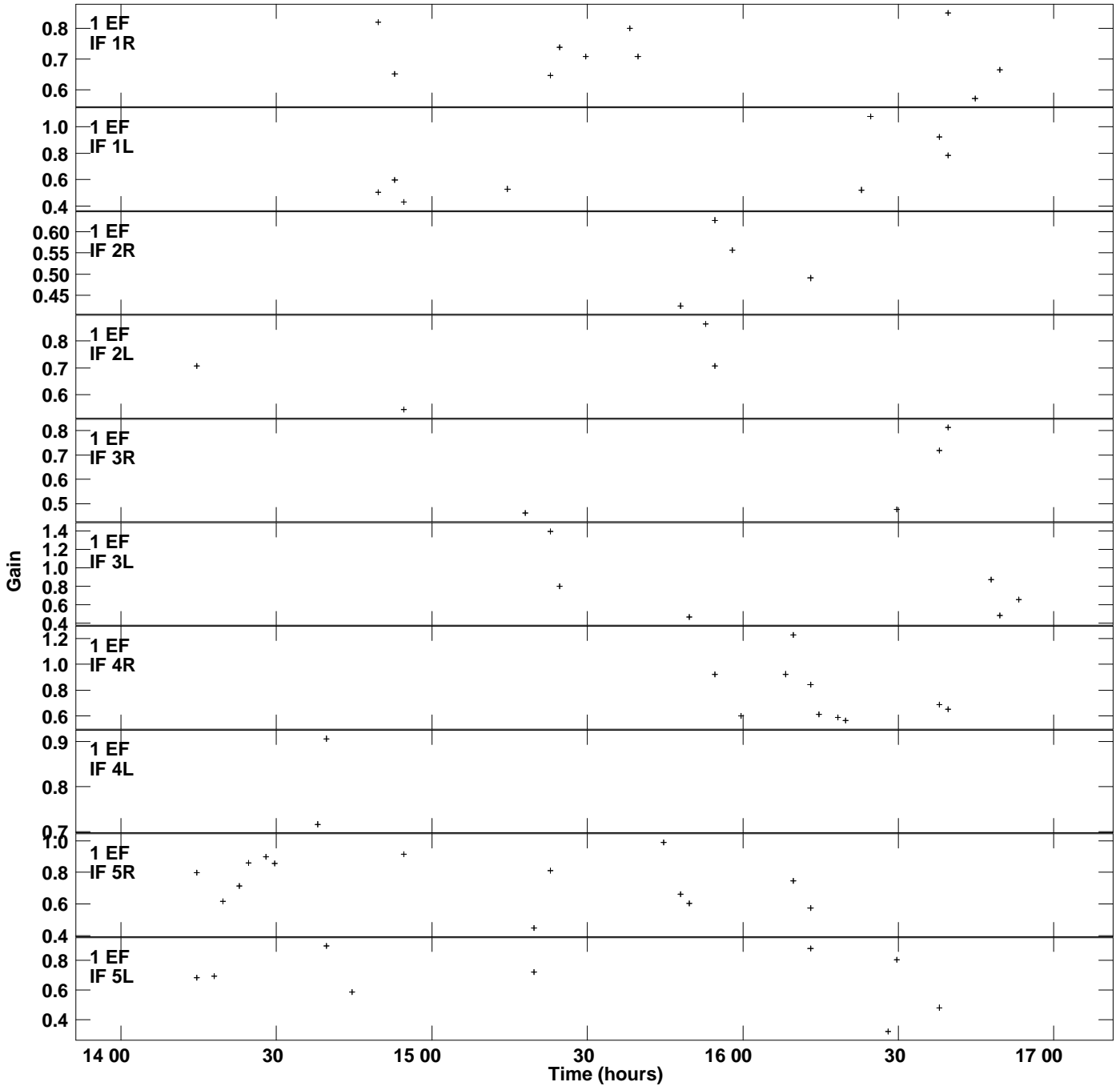
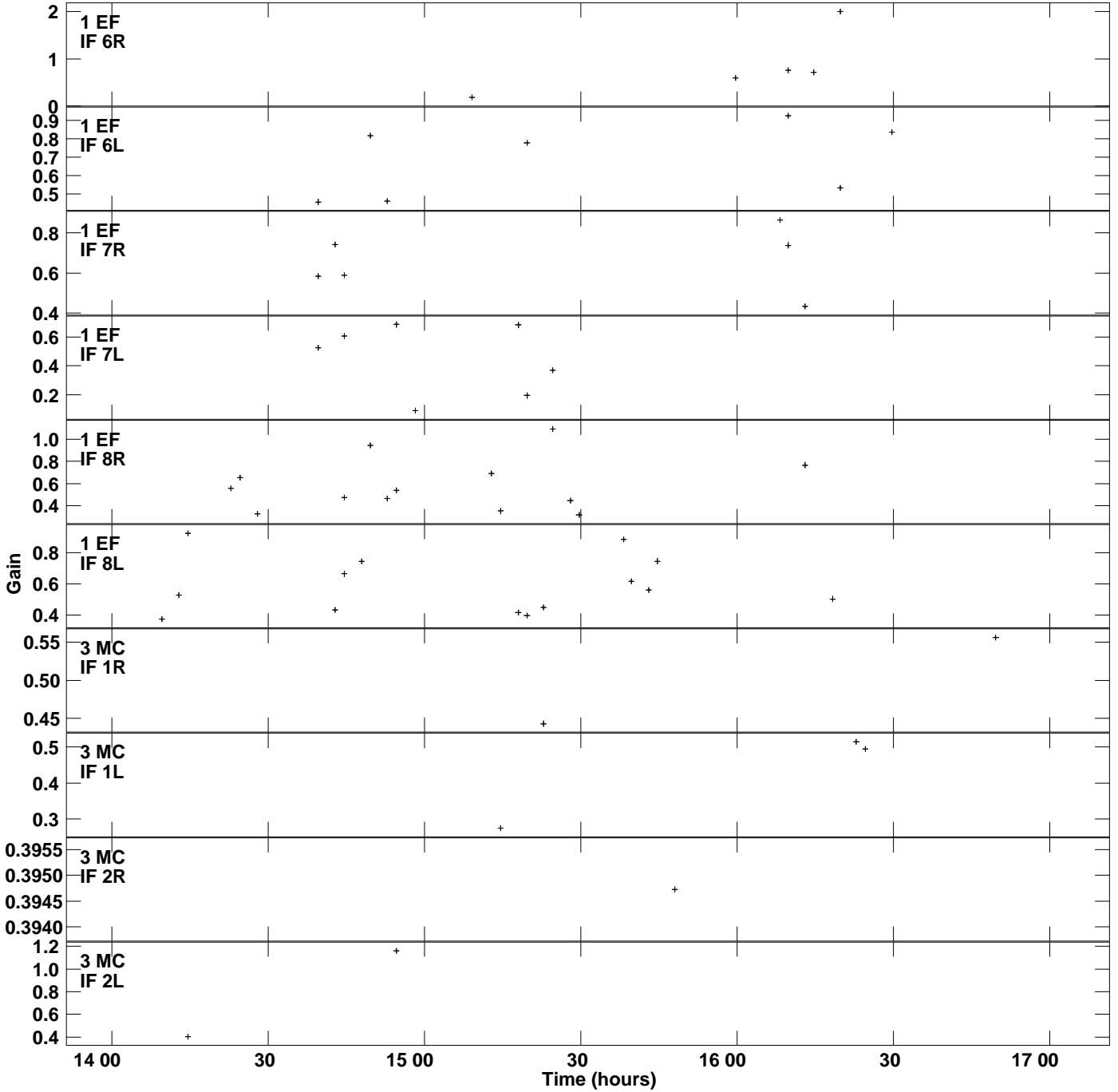


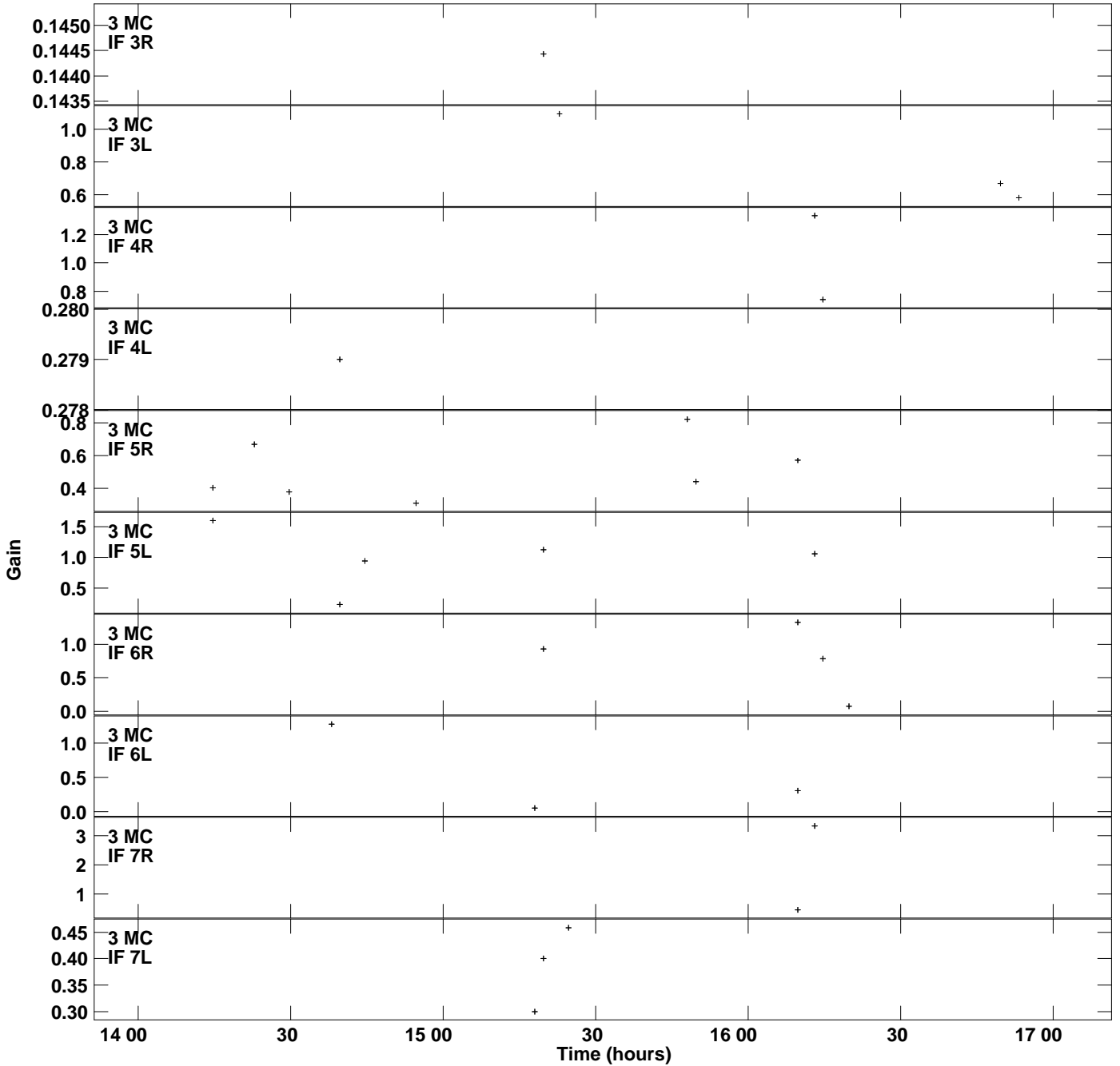
Plot file version 1 created 10-FEB-2016 17:15:28  
Gain amp vs UTC time for V1396CYG.MULTI.1  
SN 2 Rpol & Lpol IF 1 - 8



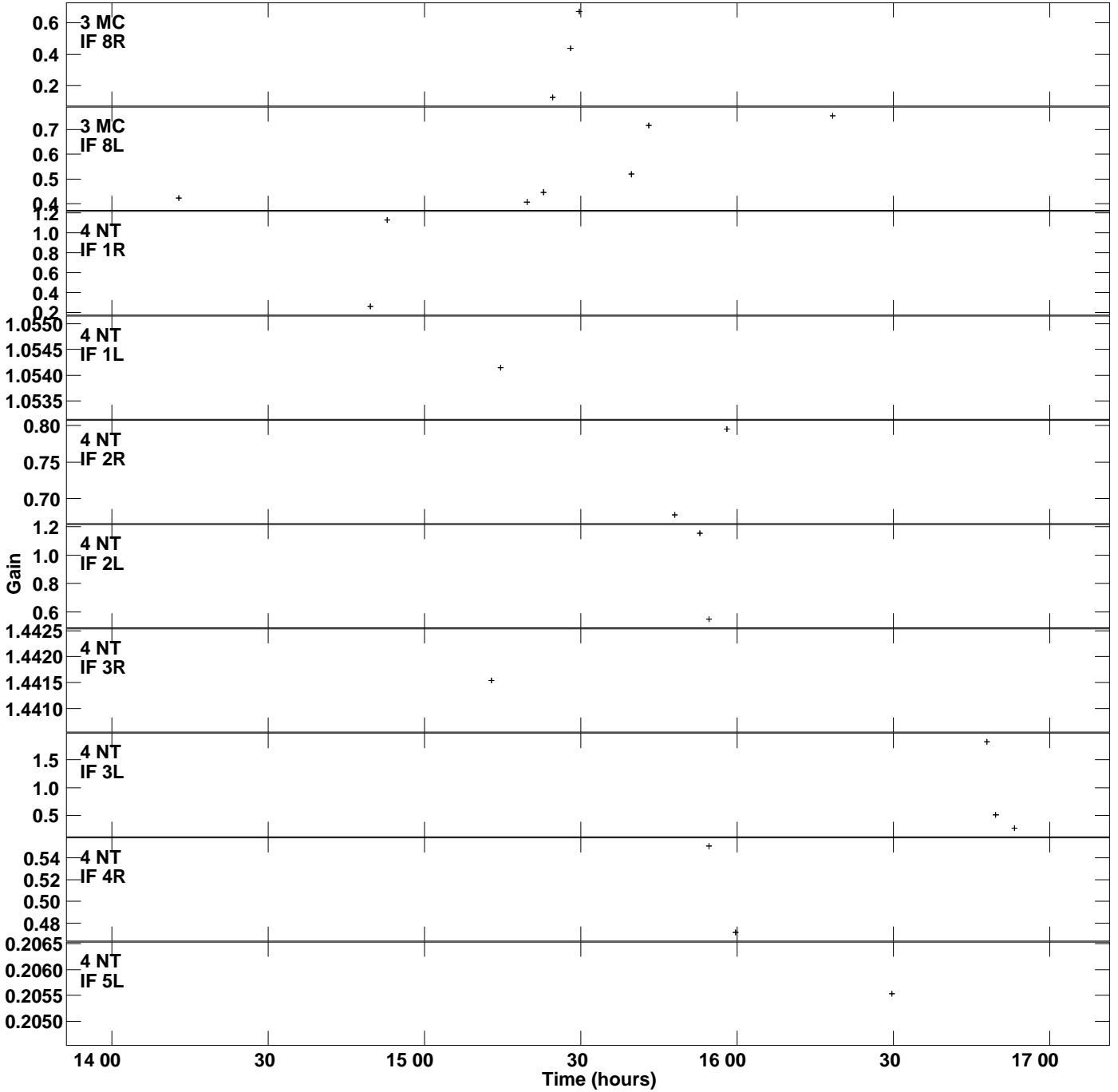
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 SN 2 Rpol & Lpol IF 1 - 8



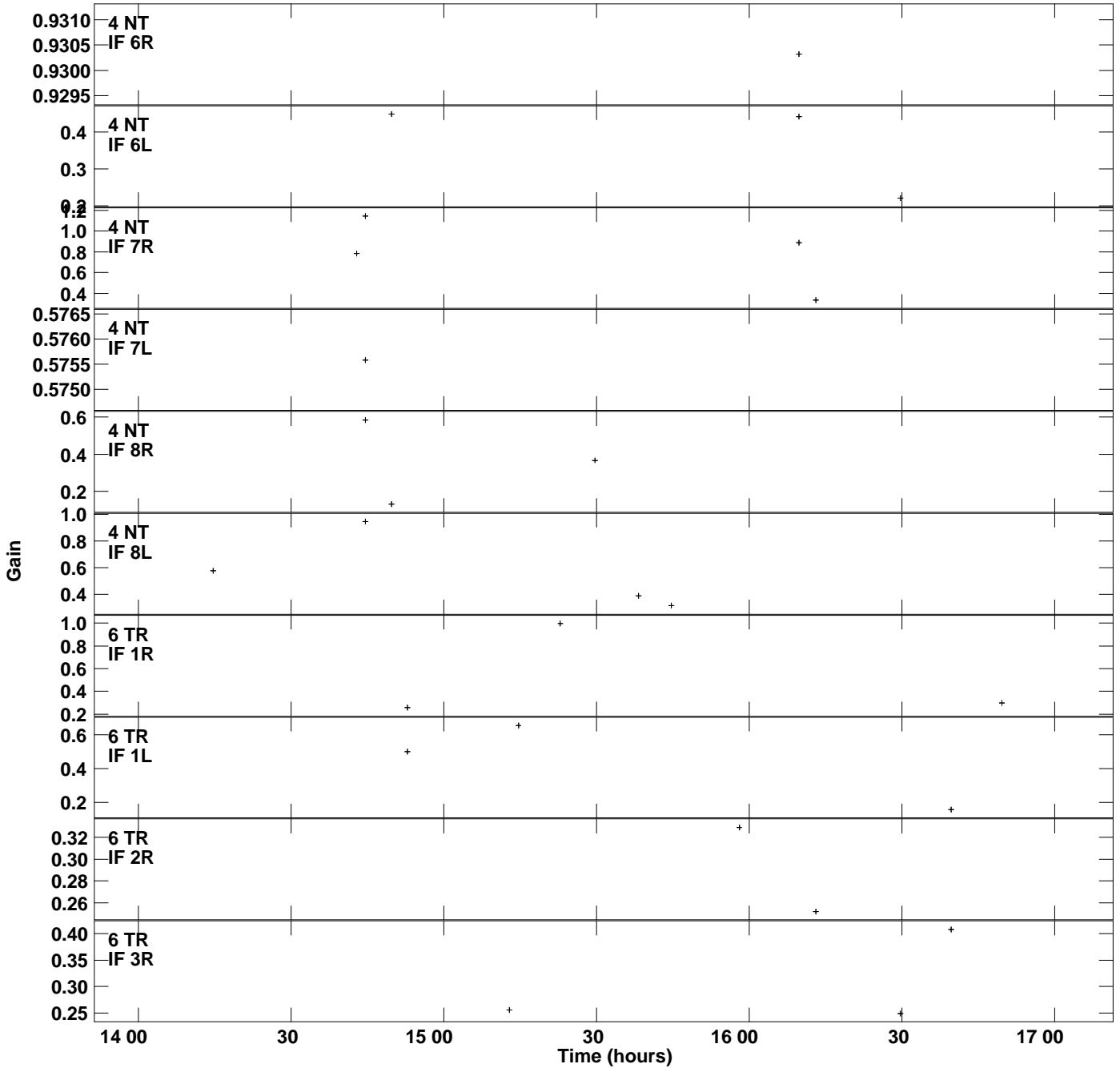
Plot file version 3 created 10-FEB-2016 17:15:28  
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 SN 2 Rpol & Lpol IF 1 - 8



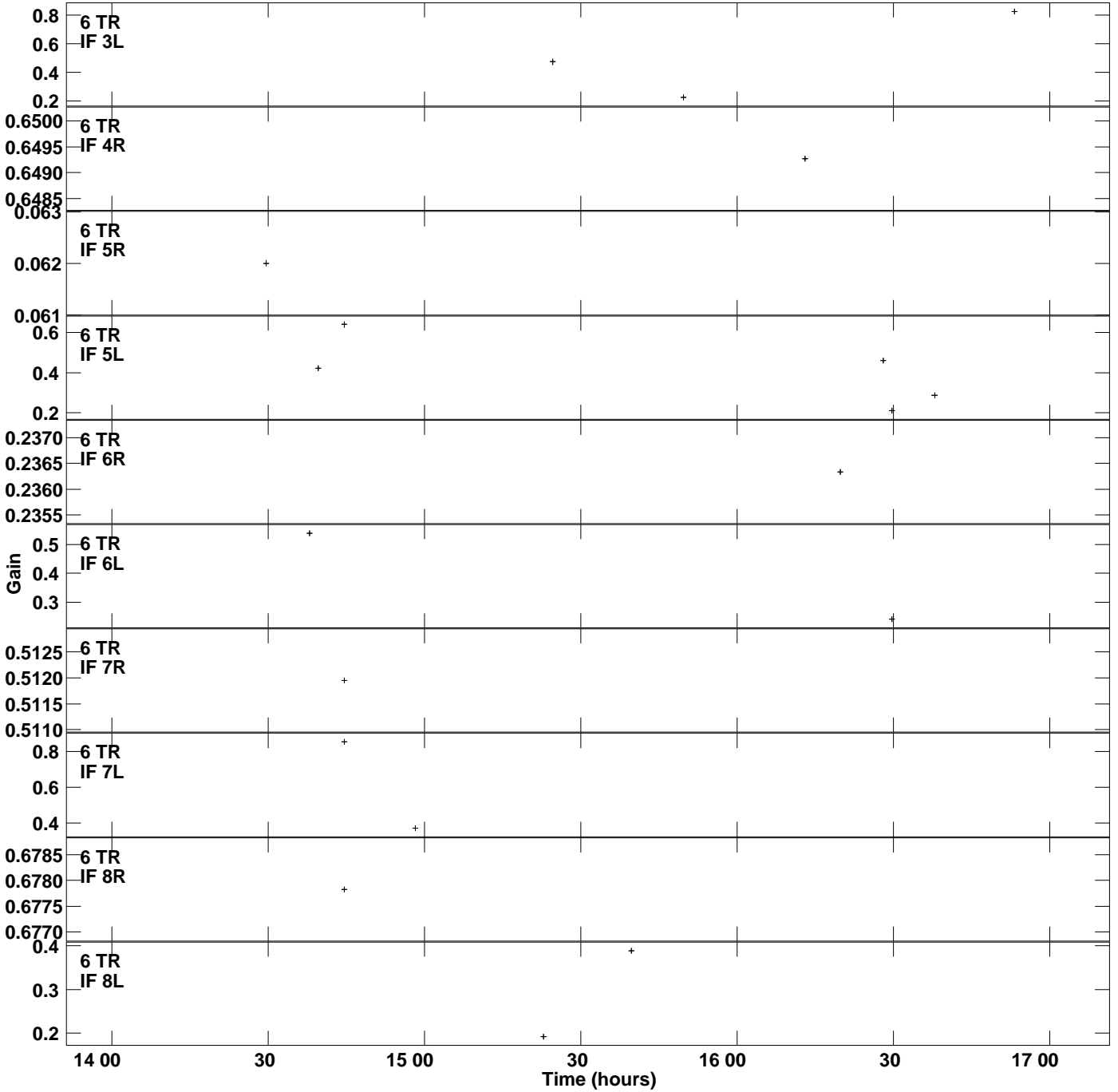
Plot file version 4 created 10-FEB-2016 17:15:28  
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 SN 2 Rpol & Lpol IF 1 - 8



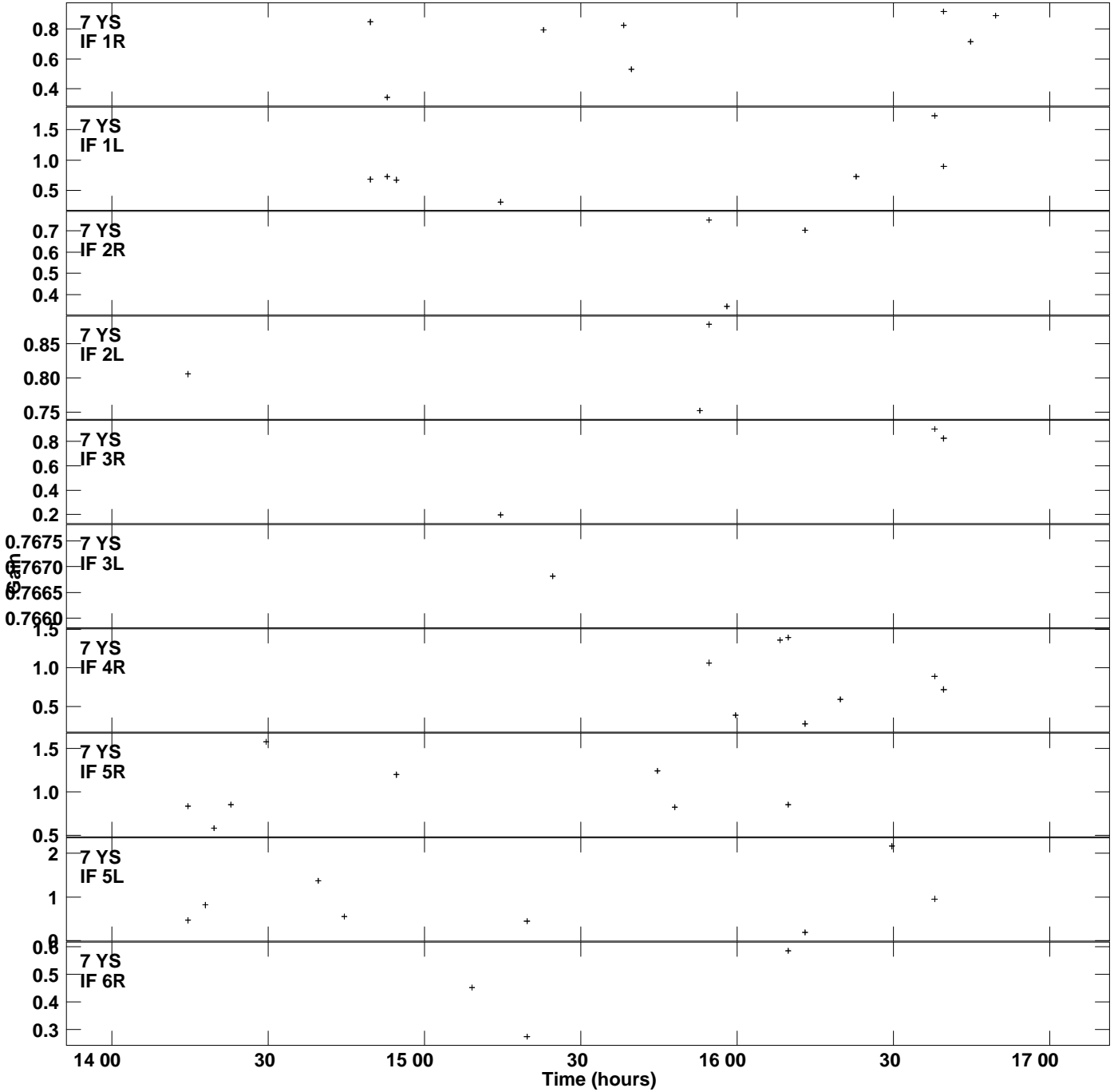
Plot file version 5 created 10-FEB-2016 17:15:28  
 Gain amp vs UTC time for V1396CYG.MULTI.1  
 SN 2 Rpol & Lpol IF 1 - 8



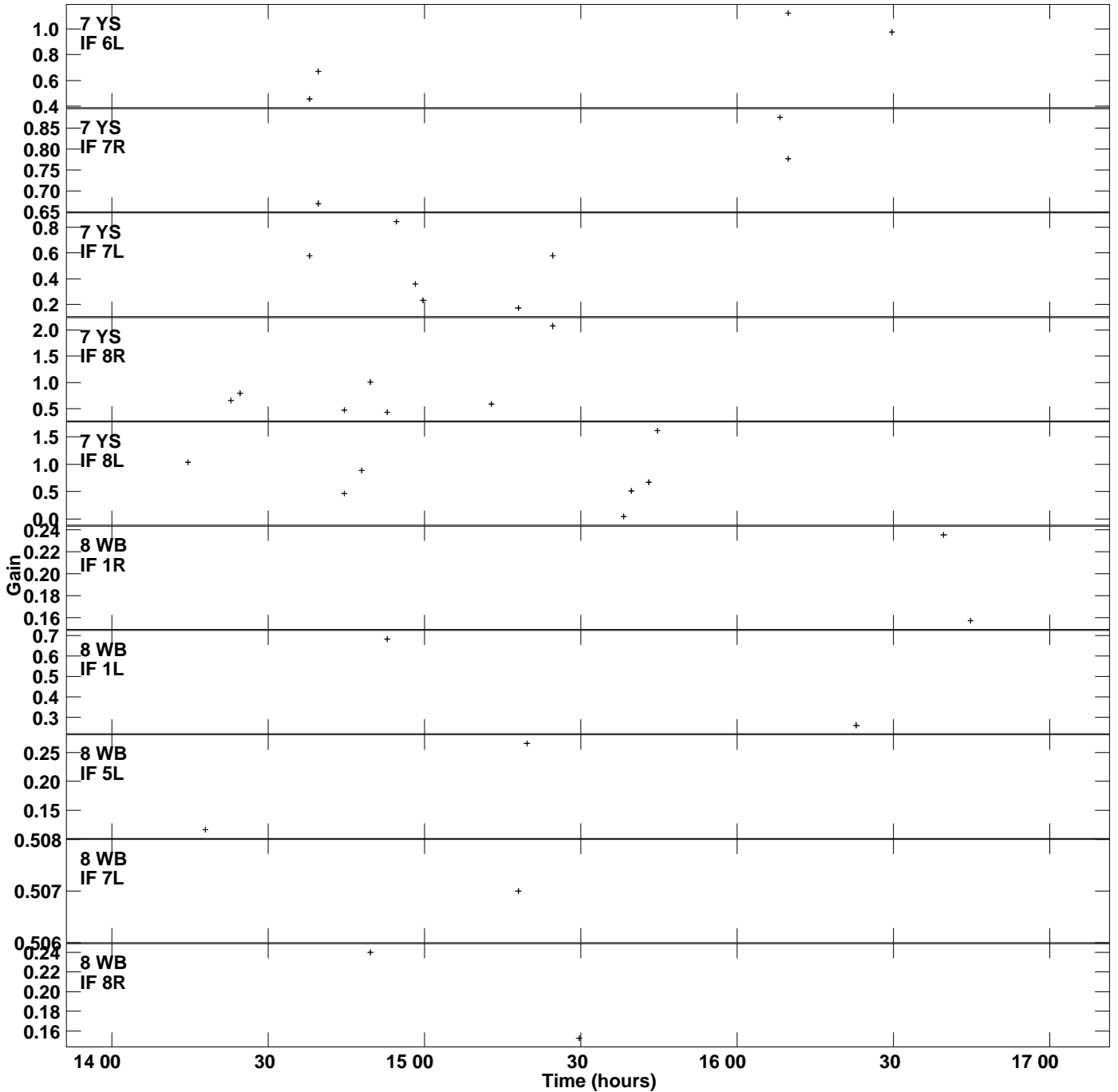
Plot file version 6 created 10-FEB-2016 17:15:28  
 Gain amp vs UTC time for V1396CYG.MULTI.1  
 SN 2 Rpol & Lpol IF 1 - 8



Plot file version 7 created 10-FEB-2016 17:15:28  
 Gain amp vs UTC time for V1396CYG.MULTI.1  
 SN 2 Rpol & Lpol IF 1 - 8



Plot file version 8 created 10-FEB-2016 17:15:28  
 Gain amp vs UTC time for V1396CYG.MULTI.1  
 SN 2 Rpol & Lpol IF 1 - 8





Plot file version 9 created 10-FEB-2016 17:15:28  
Gain amp vs UTC time for V1396CYG.MULTI.1  
SN 2 Rpol & Lpol IF 1 - 8

